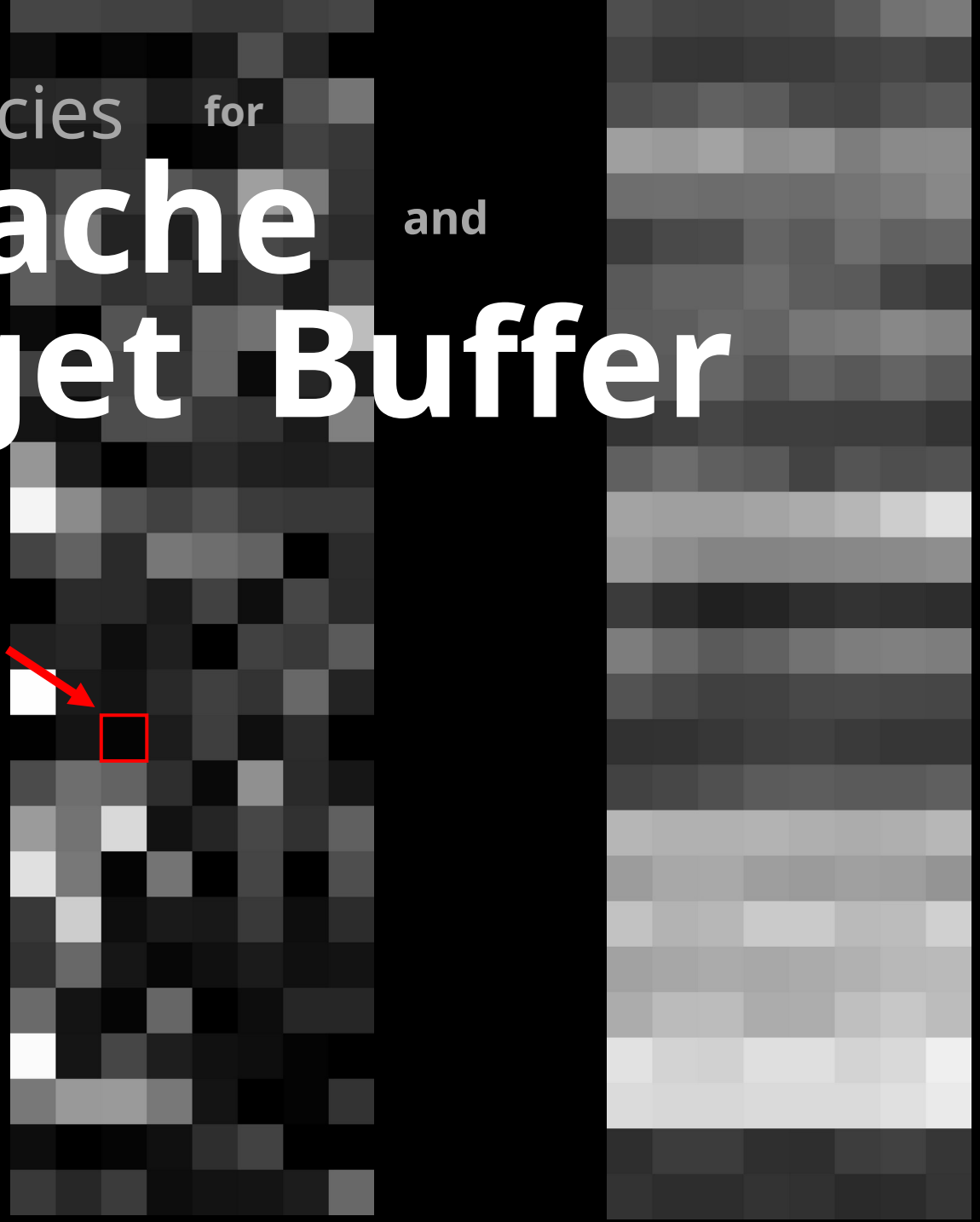
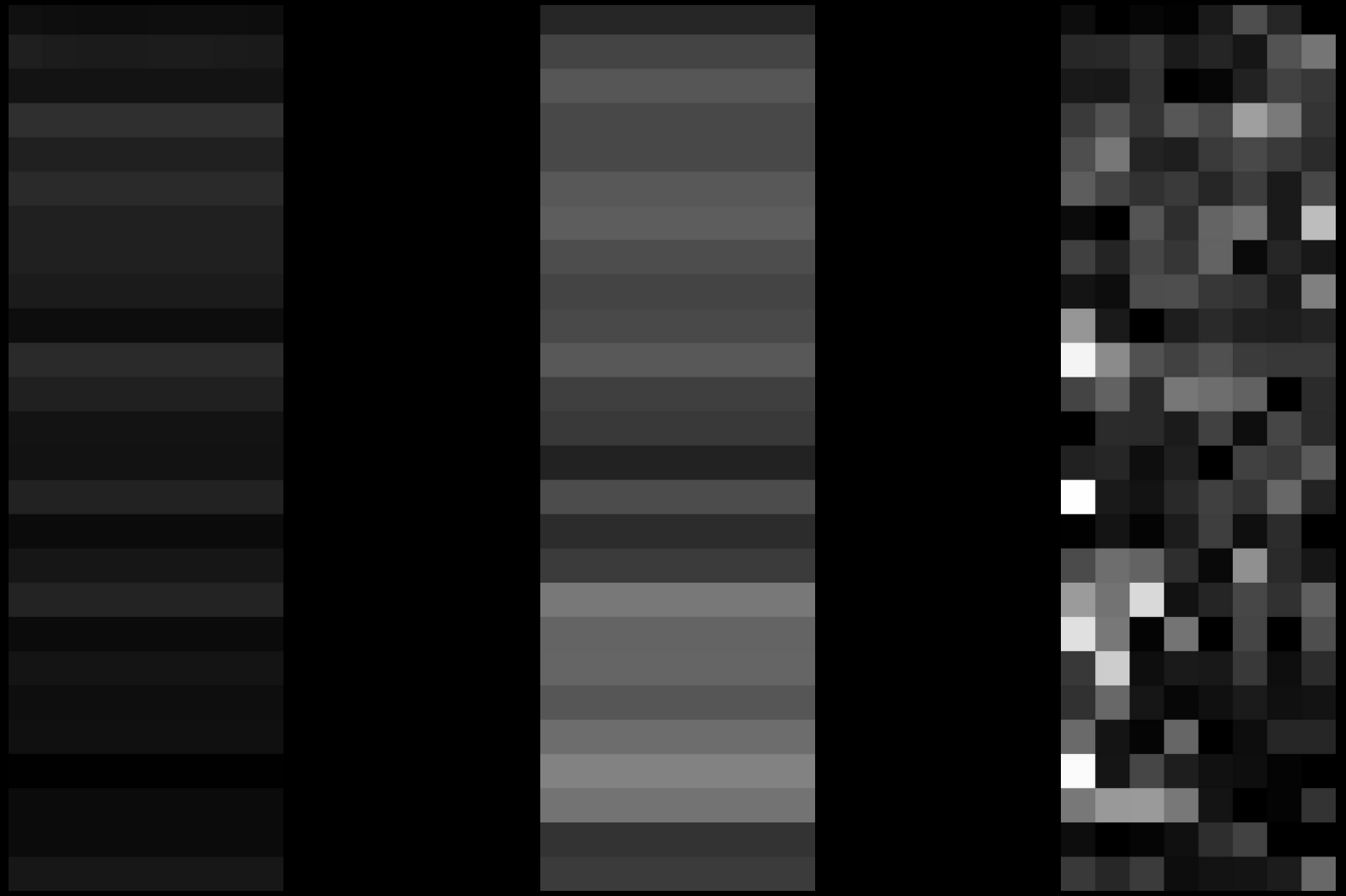
Exploring Predictive Replacement Policies for Instruction Cache and Branch Target Buffer



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Elba Garza
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COMPUTER SCIENCE & ENGINEERING
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Traditional replacement policies cannot address the problem of dead blocks

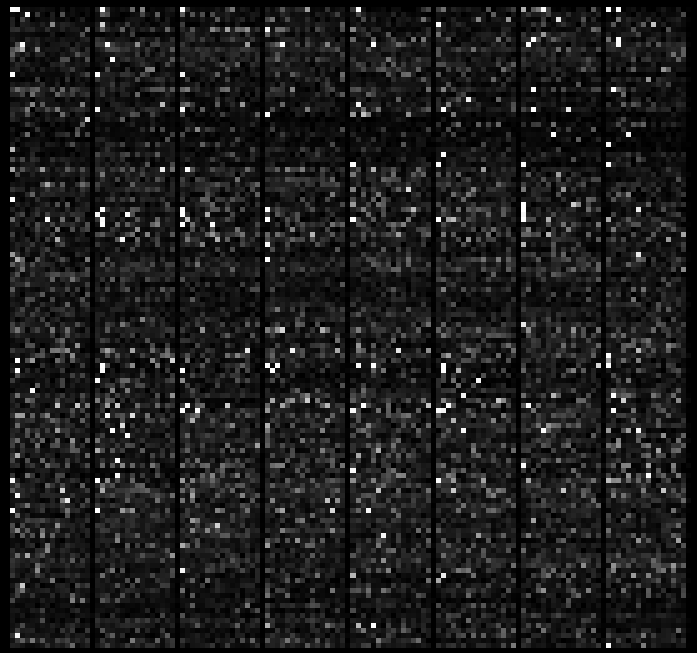


LRU Rand SRRIP

i-cache needs specific replacement policy

Many programs have high i-cache miss rate

State of art replacement policy reduces many dead blocks in LL data cache



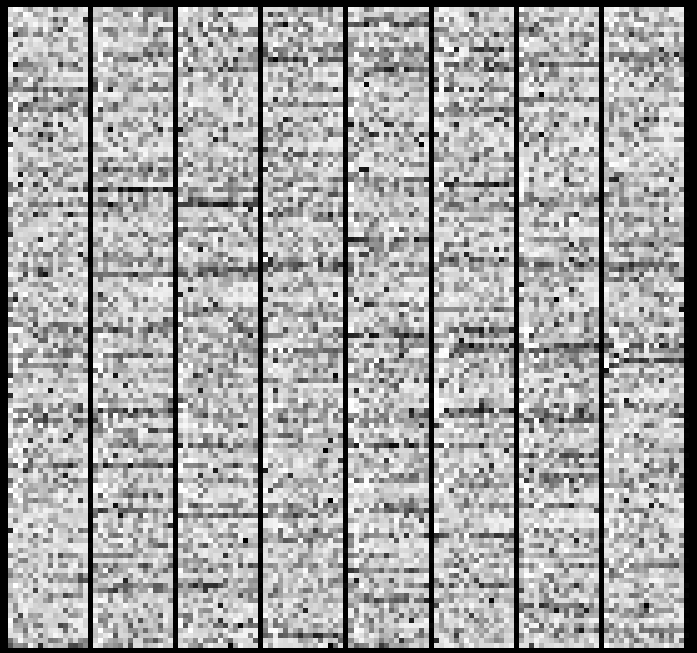


Photo Credit: Sampling Dead Block Predictor by Khan *et al.*

2:00pm Tuesday

Session 7A: Mobile Platforms (Wilshire Grand Ballroom I)

